

ABSTRACT OF THE DISCLOSURE

A system for reducing an OFF-current in a thin film transistor of a liquid crystal display device includes gate and data lines crossing each other, a pixel thin film transistor including gate, source and drain electrodes, the gate electrode connected to the gate line and the source electrode connected to the data line, a liquid crystal capacitor connected to the drain electrode of the pixel thin film transistor, a first switch thin film transistor connected to a first end of the data line, a second switch thin film transistor connected to a first end of the gate line, a first voltage source electrically connected to the drain electrode of the pixel thin film transistor, a second voltage source connected to a source electrode of the first switch thin film transistor, a third voltage source connected to gate electrodes of the first and second switch thin film transistors, and a fourth voltage source connected to a source electrode of the second switch thin film transistor.